

41CL Memory Reference

05/27/17

Page	Contents	ID	XROM	Description	YCRC value	Revision Date
0x000	NUT0-N	OS41	N/A	HP Operating System Rev N, pg 0	0xC67CA32B	02/03/14
0x001	NUT1-F			HP Operating System, Rev F, pg 1	0x8BF110D6	
0x002	NUT2-L			HP Operating System, Rev L, pg 2	0x5B1C1C70	02/03/14
0x003	XFNS3-3B	XFN3	25	HP CX Extended Functions 3B, pg 3	0xA270BAF7	11/24/14
0x004	SERVICE	HSRV	N/A	HP Service 2A	0xF023B96D	
0x005	XFNS5-2D	XFN5	N/A	HP CX Extended Functions 2D, pg 5	0x447DF70E	
0x006	TIME-3A	TMOD	26	HP CX Time Functions 3A	0xEA20E86B	02/23/13
0x007	YFNZ-4E	YFNZ	15	41CL Extra Functions 4E	0xB9E38DA4	11/21/14
0x008	ADVL1-1B	41AD	22	HP-41 Advantage Pac 1B, pg 1	0xDD3065AC	
0x009	ADVL1-1B			HP-41 Advantage Pac 1B, pg 1	0xDD3065AC	
0x00A	YFNX-2B	YFNX	15	41CL Extreme Functions 2B	0x0C020957	11/21/14
0x00B	YLIB-3A	YLIB	N/A	41CL Library Functions 3A	0x5AB2D280	12/28/15
0x00C	ADVU1-1B	41AD	24	HP-41 Advantage Pac 1B, pg 2, bnk 1	0xF7BCFBE7	
0x00D	ADVU2-1B			HP-41 Advantage Pac 1B, pg 2, bnk 2	0x2B2E2B57	
0x00E	MPL2V1F	MLTI	3	Multiprecision Library ROM	0xE3E8C1B1	08/02/11
0x00F	YFNP-1E	YFNP	15	41CL Extra Functions Plus 1E	0x8F37F473	11/21/14
0x010	AECROML	AECR	18	AECROM Module, pg 1	0x34F1B188	
0x011	AECROMU			AECROM Module, pg 2	0x31DD795C	
0x012	AUTOFIN	AFIN	21	Autofinance Module	0x2666909C	
0x013	ASSEM3	ASMB	21	Assembler 3	0x1FD481E3	
0x014	AUTOSTRT	AUTO	10	HP Autostart	0x0BB24A6A	
0x015	AVIATION	AVIA	19	HP Aviation Pac 1A	0x1A7938E1	
0x016	B52DEX	B52B	21	Boeing B-52 Module, pg 1	0xE78B04BD	
0x017	B52DIO		31	Boeing B-52 Module, pg 2	0xA014A069	
0x018	CCDL-1B	CCDR	9	CCD Module 1B, pg 1	0xD9D7EB4D	
0x019	CCDU-2B		11	CCD Module 2B, pg 2	0x4216E34B	
0x01A	CHEMUSER	CHEM	20	Chemistry Solutions book	0x03DE24BE	
0x01B	CIRCUIT	CIRC	6	HP Circuit Analysis Pac 1A	0xE3A29B2B	
0x01C	CLINICAL	CLIN	19	HP Clinical Lab & Nuclear Medicine Pac 1A	0x721745CD	
0x01D	DATAACQL	DACQ	21	HP Data Acquisition 1B, pg 1	0x94933C29	
0x01E	DATAACQU		31	HP Data Acquisition 1B, pg 2	0xE813EC77	
0x01F	DAVID-2C	DAVA	2	David Assembler 2C	0x3166EFBA	
0x020	HPILDEVL	DEVI	22	HP HP-IL Development Pac 1B, pg 1	0x991A9E79	
0x021	HPILDEVU		24	HP HP-IL Development Pac 1A, pg 2	0x40A4E414	
0x022	HPILDIAAG	DIIL	19	HP HP- IL Diagnostic	0xC8CBD00E	
0x023	DIAMOND	DMND	31	Diamond ROM	0x2DC2A47F	

0x024	ES41L	E41S	4	ES41 Module, pg 1	0x10ADCDF3
0x025	ES41U		6	ES41 Module, pg 2	0x4AABE9EE
0x026	ESMLDL	ESML	10	ESMLDL 7B Module	0xFB2A334E
0x027	EXTIO-1A	EXIO	23	HP Extended I/O 1A	0x504397A2
0x028	FINANCE	FINA	4	HP Financial Decisions Pac 1D	0x5346BC6F
0x029	GAMES-1A	GAME	10	HP Games Pac 1A	0xDA9B9799
0x02A	GMAC2	GMAS	31	GMAC 2 Module	0x524B3D27
0x02B	GMAC3L	GMAT	21	GMAC 3 Module, pg 1	0x7D266AF8
0x02C	GMAC3U		31	GMAC 3 Module, pg 2	0x41A229ED
0x02D	HEPX1-1E	HEPX	7	HEPAX 1D Module, bnk 1	0x0659DE89
0x02E	HEPX2-1D			HEPAX 1D Module, bnk 2	0xE9ACACBA
0x02F	HEPX3-1D			HEPAX 1D Module, bnk 3	0x5C548CF8
0x030	HEPX4-1D			HEPAX 1D Module, bnk 4	0x88B25201
0x031	HOMEMGT	HOME	9	HP Home Management Pac 1A	0x5C02D8D7
0x032	LABELS	LBLS		LABELS ROM	0x9E04665E
0x033	LANDNAV	LAND	1	LANDNAV Module	0xA00C3699
0x034	MATH-1D	MATH	1	HP Math Pac 1D	0xCB6B3AB2
0x035	MACHINE	MCHN	12	HP Machine Design Pac 1A	0xAFB46CDA
0x036	MELBRNE	MELB	12	Melbourne ROM	0x42B19762
0x037	MIL-ENGL	MILE	21	Military Engineering, pg 1	0x888318B4
0x038	MIL-ENGU		31	Military Engineering, pg 2	0x165D810F
0x039	MLROM	MLRM	21	MLROM	0x8C4C2123
0x03A	NAVL-1B	NAVI	14	HP Navigation Pac 1B, pg 1	0x80C504F2
0x03B	NAVU-1B		14	HP Navigation Pac 1B, pg 2	0xF2A12F3B
0x03C	NFCROM	NFCR	17	NFCROM 1B	0x93941C36
0x03D	NAVCOM2L	NVCM	14	NAVCOM 2, pg 0	0xA65D4319
0x03E	NAVCOM2U		15	NAVCOM 2, pg 1	0x4B36EC51
0x03F	APAC1	P3BC	9	Aviation Pac for P3B/C, pg 1	0xF1D2CC95
0x040	APAC2		31	Aviation Pac for P3B/C, pg 2	0x82DB2775
0x041	APAC3		21	Aviation Pac for P3B/C, pg 3	0x825B3038
0x042	APAC4		9	Aviation Pac for P3B/C, pg 4	0xF1D2CC95
0x043	PCODER	PCOD	16	Pcoder 1A Module	0xFD976C53
0x044	PETROLL	PETR	15	HP Petroleum Fluids Pac 1A, pg 1	0xC234E735
0x045	PETROLU		16	HP Petroleum Fluids Pac 2A, pg 2	0x2719C0EE
0x046	PLOTTERL	PLOT	17	HP Plotter Pac 1A, pg 1	0x7AF0E0E8
0x047	PLOTTERU		18	HP Plotter Pac 2A, pg 2	0x18BB9276
0x048	PPCL	PPCM	10	PPC Module, pg 1	0x011BEAFE
0x049	PPCU		20	PPC Module, pg 2	0x14D6A2B3
0x04A	REALESTL	REAL	11	HP Real Estate Pac 1A, pg 1	0x3AA6E48D
0x04B	REALESTU			HP Real Estate Pac 1A, pg 2	0x87E282B9
0x04C	QUATERL	QUAT	15	Quaternion Module, pg 1	0x326D5668
0x04D	QUATERU		16	Quaternion Module, pg 2	0xAC788AB2
0x04E	SECURITY	SECY	19	HP Securities Pac 1A	0x82D5290E
0x04F	STANDARD	STAN	5	HP Standard Applications Pac 1C	0xC2173B4A
0x050	SGS-GAS	SGSG	21	SGS GAS Module	0xA78F8ADF

0x051	SIM12L	SIMM	4	SIM Module, pg 1	0x0648D8D5	
0x052	SIM12U		10	SIM Module, pg 2	0x5528FE19	
0x053	SIM34L		31	SIM Module, pg 3	0xF183D28F	
0x054	SIM34U		30	SIM Module, pg 4	0x31302A25	
0x055	SKWIDBC	SKWD	8	SKWID ROM	0xB1EBDE82	
0x056	SIMPLEX	SMPL	16	Simplex Module	0xA6D345E5	
0x057	STAT-1B	STAT	2	HP Statistics Pac 1B	0xA71163CA	
0x058	STRESS	STRE	8	HP Stress Analysis Pac 1A	0x86A0AD50	
0x059	STRUCTL	STRU	7	HP Structural Analysis Pac 1B, pg 1	0x10D00FE5	
0x05A	STRUCTU		19	HP Structural Analysis Pac 1A, pg 2	0x0C707646	
0x05B	SUPRROML	SUPR	21	SUP-R-ROM, pg 1	0x9F5748AE	
0x05C	SUPRROMU		31	SUP-R-ROM, pg 2	0x5F352490	
0x05D	SURVEY	SURV	3	HP Survey Pac 1B	0x006F7876	
0x05E	THERMAL	THER	13	HP Thermal & Transport Science Pac 1A	0x9D9C85F6	
0x05F	TOOLBOX3	TOOL	13	Toolbox III ROM	0x80DE201D	02/17/12
0x060	USPSL	USPS	21	USPS Module, pg 1	0x22F93A01	
0x061	USPSU		31	USPS Module, pg 2	0xA99532C3	
0x062	YFNS-4E	YFNS	31	41CL Extra Functions (XROM #31)	0x985A987B	11/21/14
0x063	41ZL	Z41Z	1	HP41Z Complex Number ROM, pg 1	0x3E9F221C	11/01/11
0x064	41ZU		4	HP41Z Complex Number ROM, pg 2	0xBE58C159	11/01/11
0x065	YREGAPPS	YRGA	21	YREG Applications ROM	0xC71C310B	08/25/16
0x066	HANDY	HNDY	31	Handy Compact ROM	0x5C5FE236	07/07/16
0x067	ASTRO-1	ASTT	6	Astro-2010 ROM, pg 1	0x534B45A3	
0x068	ASTRO-2			Astro-2010 ROM, pg 2	0xEB815A99	
0x069	ASTRO-3			Astro-2010 ROM, pg 3	0x5D9DB525	
0x06A	ASTROUI		8	Astro-2010 UI ROM	0x62510A17	
0x06B	CHESS1	CHES	8	Chess ROM, pg 1	0xD285F4BD	05/05/17
0x06C	CHESS2			Chess ROM, pg 2	0x7B042C6D	05/05/17
0x06D	FUN1	FUNS	10	Funstuff ROM, pg 1	0xADDE16CE	06/10/16
0x06E	FUN2			Funstuff ROM, pg 2	0xB8B36961	06/10/16
0x06F	FUN3			Funstuff ROM, pg 3	0x702C8E1C	12/24/16
0x070	FUN4			Funstuff ROM, pg 4	0xB4F6F8D1	06/10/16
0x071	JBMATHL	JMAT	5	JMB-Math ROM, pg 1	0x3B320E23	
0x072	JBMATHU		6	JMB-Math ROM, pg 2	0x89ABC04	
0x073	SLANTR	SR1B	11	SLANTR-SR1B ROM	0x1CEEE2D7	06/03/16
0x074	QUEENS	QUEN	9	N-Queens ROM	0x8FD86EED	05/12/16
0x075	KRAUSS1	KRSS	17	Krauss book, pg 1	0x5A1BD088	05/12/16
0x076	KRAUSS2			Krauss book, pg 2	0x0E21B262	05/12/16
0x077	TREKKIES	TREK	11	Trekkies ROM	0x80DF47BF	
0x078	UNITCONV	UNIT	10	UNITCONV ROM	0xE8A981FA	01/25/17
0x079	BMF1	ADV1	12	Adventure ROM, pg 1	0xE441BEE7	06/01/16
0x07A	BMF2			Adventure ROM, pg 2	0xCBED8E13	06/01/16
0x07B	BMF3			Adventure ROM, pg 3	0xED8C5C95	06/01/16
0x07C	BMF4			Adventure ROM, pg 4	0x4D7257DF	06/01/16

0x07D	BMF5	ADV2	13	Adventure ROM, pg 5	0x7C1B6D51	06/01/16
0x07E	BMF6			Adventure ROM, pg 6	0x6EA0A209	06/01/16
0x07F	BMF7			Adventure ROM, pg 7	0xC15E821D	06/01/16
0x080	BMF8			Adventure ROM, pg 8	0xE458FDA4	06/01/15
0x081	CFIT1	CURV	4	CurveFit ROM, pg 1	0x9D0A8240	01/12/17
0x082	CFIT2		4	CurveFit ROM, pg 2	0x541ADB05	01/12/17
0x083	MATRIX1	JMTX	8	JMB-Matrix ROM, pg 1	0xADC5E87B	
0x084	MATRIX2		8	JMB Matrix ROM, pg 2	0x885C62C5	
0x085	DISASM4D	DASM	15	DisAsm 4D ROM	0xF34E5920	
0x086	EXT-IL	EXTI	27	SKWID EXT-IL ROM	0xD2BDDA53	
0x087	CCD-OSX	CCDX	5	CCD-OSX ROM	0x189D74C4	
0x088	SANDBOXL	SBOX	8	Sandbox ROM, pg 1	0xAC8E208F	
0x089	SANDBOXU		13	Sandbox ROM, pg 2	0x50D13F58	
0x08A	PANAMEL	PANA	5	Paname ROM, pg 1	0x1835E11B	
0x08B	PANAMEU		9	Paname ROM, pg 2	0x94B6C549	
0x08C	83TRINH	TRIH	9	83trinh ROM	0x0C499145	
0x08D	ADV-APP	AADV	19	Advantage Applications ROM	0x4CD371AC	
0x08E	AFDC1L	AFDE	16	AFDC1 ROM, pg 1	0xE8F9A42E	
0x08F	AFDC1U		17	AFDC1 ROM, pg 2	0x84C6B256	
0x090	AFDC2L	AFDF	18	AFDC2 ROM, pg 1	0xD8530171	
0x091	AFDC2U		19	AFDC2 ROM, pg 2	0x69A18873	
0x092	AMC-OSX	AOSX	5	AMC-OSX ROM	0xCD0E8B09	
0x093	ASSEMB4	ASM4	21	Assembler 4 ROM	0x7DD19E36	
0x094	ASTROLOGY	ALGY	31	Astrology ROM	0xCA478918	
0x095	AV1	AV1Q	31	AV1 ROM	0xC2AD790B	
0x096	BCMW	BCMW	8	BCMW ROM	0x5BDE4368	
0x097	CVPAK1	CVPK	21	CVPAK ROM, pg 1	0xE2B0EC58	
0x098	CVPAK2		31	CVPAK ROM, pg 2	0x6467CCA7	
0x099	DYERKA	DYRK	31	Dyerka ROM	0xE58D1EBC	
0x09A	FORTH4	FRTH	N/A	HP-41 FORTH ROM, pg 1	0xC9EC9EC9	
0x09B	FORTH5			HP-41 FORTH ROM, pg 2	0x95445553	
0x09C	HYDRACMP	HCMP	21	Hydracomp ROM	0x3FF4FA5C	
0x09D	ICODE	ICOD	19	Icode ROM	0x2E9ECE57	08/09/11
0x09E	LAITRAM	LAIT	N/A	Laitram-XQ2 ROM	0x3DC83258	
0x09F	MCTEST	MTST	3	Mctest ROM	0x19AF1854	
0x0A0	MDP1L	MDP1	15	MDP1 ROM, pg 1	0x8C813FE0	
0x0A1	MDP1U		16	MDP1 ROM, pg 2	0x73C7B514	
0x0A2	MDP2L	MDP2	17	MDP2 ROM, pg 1	0x64BBB478	
0x0A3	MDP2U		18	MDP2 ROM, pg 2	0xEBABFF38	
0x0A4	MUECKEL	MUEC	21	Muecke ROM, pg 1	0xA9DC169B	
0x0A5	MUECKEU		31	Muecke ROM, pg 2	0x7B158017	
0x0A6	NAVPAC2L	NPAC	14	Navpac ROM, pg 1	0xEE05C28B	
0x0A7	BW1A		15	Navpac ROM, pg 2	0xFD2DFEA5	
0x0A8	OILWELL	OILW	21	Oilwell ROM, pg 1	0x8502C885	
0x0A9	OILWELLU		31	Oilwell ROM, pg 2	0x88D42414	

0x0AA	PARIO	PARI	14	Pario ROM	0x3E2F63E6	
0x0AB	PSOF	PSOF	16	PSOF ROM	0xF7E1D7B0	09/15/11
0x0AC	PPC-MELB	PMLB	12	PPC-Melbourne ROM	0x8A7ECB80	
0x0AD	PRIDE1	PRIQ	21	Pride ROM, pg 1	0x45250A0E	
0x0AE	PRIDE2		31	Pride ROM, pg 2	0xD4942AED	
0x0AF	ROAM-0A	ROAM	5	ROAM ROM	0x04D3BC5B	08/09/11
0x0B0	ROMSV01	ROMS	9	Romsv01 ROM	0x95D8881F	
0x0B1	SEAKING	SEAK	21	SeaKing ROM	0x0F256BCD	07/31/11
0x0B2	SPEED2L	SMCH	21	Speed Machine II ROM, pg 1	0xEBCA6C99	
0x0B3	SPEED2U		31	Speed Machine II ROM, pg 2	0x3DB3FFFF	
0x0B4	TOMSROM	TOMS	6	Tomsrom ROM	0xC894F0F9	
0x0B5	ZENROM	ZENR	5	Zenrom ROM	0x02FB484B	
0x0B6	ZEPROM	ZEPM	9	Zeprom ROM	0x79543B69	
0x0B7	TURBOL	CCDP	9	CCD Plus ROM, pg 1	0xF9A2ABB4	
0x0B8	TURBOU		11	CCD Plus ROM, pg 2	0x022C6C4C	
0x0B9	HEPRAM	HEPR	N/A	HEPAX RAM Template	0x2273144F	
0x0BA	DISASM4C	DA4C	15	DisAsm 4C ROM	0x5944FC8E	
0x0BB	MLONDON	5LON	30	London Tube Map (for METX)	0x616E44B8	04/30/17
0x0BC	SPECTRAL	SPEC	8	Spectral Analysis ROM	0x1DD2785C	
0x0BD	KNIGHTTR	KNGT	31	JMB Knight's Tour ROM	0x9B3FE97D	05/10/16
0x0BE	MARKS3A	DBUG	3	(RAM only) HP-41 MCODE Debugger ROM	0x8C67EE11	06/08/16
0x0BF	MLABELS	MLBL	N/A	Mainframe Labels ROM	0x9E04665E	07/31/11
0x0C0	BESSEL1	BESL	2	Bessel Functions ROM, pg 1	0x939B2BF8	
0x0C1	BESSEL2		3	Bessel Functions ROM, pg 2	0xF294ABEF	
0x0C2	POLYN1	POLY	6	Polynomial Functions ROM, pg 1	0x9154C56E	
0x0C3	POLYN2		9	Polynomial Functions ROM, pg 2	0xA8E8AFAA	
0x0C4	ILBUFFER	ILBF	22	IL Buffer ROM	0xE7A8790F	
0x0C5	NOVCHAP	NCHP	31	NOV CHAP ROM	0xDC06E071	
0x0C6	MPATHS	METX	30	MetroX ROM	0xFD703F97	04/30/17
0x0C7	MMADRID	5MAD		Madrid Metro Map (for METX)	0x185C3372	04/24/17
0x0C8	DIFFGEOM	GEOD	31	Differential Geometry ROM	0xF1B9A63D	08/28/16
0x0C9	ISENE	ISEN	17	ISENE ROM	0x3C0BD981	10/24/11
0x0CA	BSMS	BSMS	18	Bus. Sales/Marketing/Stats Solution Book	0xD07852CA	10/24/11
0x0CB	CNTLSYS	CNTL	14	Control Systems Solution Book	0x219299E9	03/03/13
0x0CC	ELECENG	EENG	15	Electrical Engineering Solution Book	0x8FD0F0A8	10/30/11
0x0CD	LENDLS	LNDL	19	Lend/Lease Savings Solution Book	0x7A014988	10/24/11
0x0CE	TESTSTAT	TEST	13	Test Statistics Solution Book	0xA82CB38F	11/10/16
0x0CF	MECHENG	MENG	16	Mechanical Engineering Solution Book	0xDA5B87B3	10/26/16
0x0D0	XROM	ROMX	31	XROM ROM	0xDAAD35AD	04/29/17
0x0D1	ANTENNAS	ANTS	16	Antennas Solution Book	0x60867B7C	02/25/16
0x0D2	OPTOMTR	OPTO	16	Optometry Solution Book	0x7BB923E5	10/31/11
0x0D3	PHYSICS	PHYH	15	Physics Solution Book	0x283DF261	10/26/16
0x0D4	GEOMETRY	GEOM	14	Geometry Solution Book	0x2D53B753	11/22/11
0x0D5	HL_MATH	HMAT	12	High-Level Math Solution Book	0xFBF8945B	11/22/11
0x0D6	INTSOLN	ISOL	11	Interchangeable Solutions UPLE Program	0xBE1FDEAC	02/10/17

0x0D7	TIMERSLN	TIME	6	Timer Solution Book	0x902B26A1	11/04/11
0x0D8	GRAFIKS1	GRF1	9	Grafiks ROM, pg 1	0x7D34373D	08/27/16
0x0D9	GRAFIKS2			Grafiks ROM, pg 2	0xEE12B8E5	07/27/16
0x0DA	GRAFIKS3	GRF3	10	Grafiks ROM, pg 3	0x45F45FF1	07/27/16
0x0DB	GRAFIKS4			Grafiks ROM, pg 4	0x3F515E7B	07/27/16
0x0DC	CAB41L	CAB4	21	Schenk CAB 41 ROM, pg 1	0x1AF4A9EA	08/04/16
0x0DD	CAB41U		31	Schenk CAB 41 ROM, pg 2	0x688AC431	08/04/16
0x0DE	FLDB	FLDB	N/A	41CL Flash YCRC Database	0x05272017	05/27/17
0x0DF	IMDB	IMDB	N/A	41CL Image Database	0x32F2A99B	05/27/17
0x0E0	GASPRP1	GASL	1	GASPROP ROM, pg 1	0x0DA56A8A	07/27/16
0x0E1	GASPRP2		2	GASPROP ROM, pg 2	0x231A1D20	07/27/16
0x0E2	GPROPP3		3	GASPROP ROM, pg 3	0x6A32AD99	07/27/16
0x0E3	GPROPP4		4	GASPROP ROM, pg 4	0xBA1C1417	07/27/16
0x0E4	GPROPP5	GASU	5	GASPROP ROM, pg 5	0xA9BC8A12	07/27/16
0x0E5	GASPRP6		6	GASPROP ROM, pg 6	0xDB0B714F	07/27/16
0x0E6	GPROPP7		8	GASPROP ROM, pg 7	0x7DCF8B26	07/27/16
0x0E7	GPSERVIC		7	GASPROP ROM, pg 8	0x59960EE8	07/27/16
0x0E8	AEC3L	AEC3	18	AECROM III Module, pg 1	0x1B79FD88	08/23/11
0x0E9	AEC3U		18	AECROM III Module, pg 2	0x5249CCAD	08/23/11
0x0EA	BLDROM	BLDR	17	BLD ROM	0xD7EAF53A	08/02/11
0x0EB	FACCL	FACC	10	300889 FACC ROM, pg 1	0xFBB19D07	08/25/11
0x0EC	FACCU		11	300889 FACC ROM, pg 2	0x9B205EEC	08/25/11
0x0ED	BGUG_IDC	IDC1	21	BGUG ROM	0xF0E36AD4	07/31/11
0x0EE	ML-IDC_L	IDC2	21	ML-IDC ROM, pg 1	0xACBEEA79	07/31/11
0x0EF	ML-IDC_U		22	ML_IDC ROM, pg 2	0x35F9AC9D	07/31/11
0x0F0	KC135_1L	K135	21	KC135 Weight & Balance ROM, pg 1	0xC69D63BD	09/13/11
0x0F1	KC135_1U		31	KC135 Weight & Balance ROM, pg 2	0xE6E1F56A	09/13/11
0x0F2	KC135_2		16	KC135 Weight & Balance ROM, pg 3	0xC19C79A9	09/13/11
0x0F3	ALL_00				Blank, loaded as KC135 W&B ROM, pg 4	0xD666D0E4
0x0F4	PROFIL	PRFS	27	Profiset ROM, pg 1	0x0F3423F1	08/02/11
0x0F5	PROFIU		31	Profiset ROM, pg 2	0x1D013B09	08/02/11
0x0F6	SMATH2L	SND2	2	Sandmath II ROM, pg 1	0xB35356D4	07/13/12
0x0F7	SMATH2U		3	Sandmath II ROM, pg 2	0xDB82EFF5	07/13/12
0x0F8	MAHJONG1	MAHJ	10	Mahjong ROM, pg 1	0xE176B636	06/02/16
0x0F9	MAHJONG2			Mahjong ROM, pg 2	0x2C60830F	06/02/16
0x0FA	IERF1	IERR	1	IERF ROM, pg 1	0x54E81895	04/22/13
0x0FB	IERF2			IERF ROM, pg 2	0xD3941CC8	04/22/13
0x0FC	ACTION1	AGAM	13	Action Games ROM, pg 1	0x436ACA57	06/06/16
0x0FD	ACTION2			Action Games ROM, pg 2	0x3EDC15A8	06/06/16
0x0FE	L119L	L119	21	AFDC-L119 ROM, pg 1	0xE19AFE10	07/31/11
0x0FF	L119U		31	AFDC-L119 ROM, pg 2	0x7BD2A484	07/31/11
0x100	ADVMTRX1	MTRA	22	Advanced Matrix ROM, pg 1, bnk 1	0x68B79323	02/16/12
0x101	ADVMTRX1		24	Advanced Matrix ROM, pg 1, bnk 2	0x68B79323	02/16/12
0x102	CCDADV	CCDA	10	CCD Advanced ROM	0x706C0B22	02/17/12
0x103	GEOMETRY	GMTY	16	Geometry 11 ROM	0x1C7F4143	02/16/12

0x104	ADVMTRX2	MTRA	22	Advanced Matrix ROM, pg 2, bnk 1	0xF36D67ED	02/16/12
0x105	ADVMTRX3		24	Advanced Matrix ROM, pg 2, bnk 2	0x379659C1	02/16/12
0x106	NUMTHRY	NTHY	16	Number Theory ROM	0x1A82B478	02/17/12
0x107	DIGITPAC	DIGT	24	DigitPAC ROM	0xE8AD50A6	02/16/12
0x108	ILDEV2L	DEV2	22	IL Development II ROM, pg 1	0x1F8D9268	02/17/12
0x109	ILDEV2U		24	IL Development II ROM, pg 2	0x3A0ACD5E	02/17/12
0x10A	MONOPLY1	MONO	16	MONOPOLY ROM, pg 1	0x5F4A9C7D	02/16/12
0x10B	MONOPLY2			MONOPOLY ROM, pg 2	0xB3FBC23A	02/16/12
0x10C	RAMBOX32	RM32	31	RAMBox32 ROM	0xB87F92B8	02/17/12
0x10D	RAMPAGE	RMPG	15	RAMpage ROM	0x74520970	02/16/12
0x10E	SUDOKU	SUD1	16	SUDOKU ROM	0x2D95C87A	02/17/12
0x10F	VECTORS	VECT	14	Vector Analysis ROM	0x92859384	02/16/12
0x110	HDISASM	HDIS	9	HEPAX Disassembler	0x76DAFD70	04/01/14
0x111	YACHTL	YACH	21	Bobby Schenk's Yacht Module, pg 1	0x09C527D7	02/17/12
0x112	YACHTU		31	Bobby Schenk's Yacht Module, pg 2	0xF317427F	02/17/12
0x113	EXILPS	EILP	27	Extended IL Plus ROM	0xF8017E3A	02/23/12
0x114	CLUTLSB1	PWRL	12	Power-CL Utilities, bnk 1	0x719402FF	11/22/14
0x115	CLUTLSB2			Power-CL Utilities, bnk 2	0x74EB21F2	09/17/14
0x116	CLUTLSB3			Power-CL Utilities, bnk 3	0x0791F2E5	11/22/14
0x117	CLUTLSB4			Power-CL Utilities, bnk 4	0x61BC9BC5	11/22/14
0x118	DIFEQ1	DIFF	15	Differential Equations ROM, pg 1	0xFA5F8A3C	05/30/12
0x119	DIFEQ2			Differential Equations ROM, pg 2	0xA0E2C68B	05/30/12
0x11A	INTEG1	INTG	16	Integrator ROM, pg 1	0x317B7E69	05/30/12
0x11B	INTEG2			Integrator ROM, pg 2	0x53AEDC2E	05/30/12
0x11C	SIROM	SIHP	24	SI ROM	0x14F0CD12	02/10/17
0x11D	PRTSRVC	PSRV	N/A	Printer Service ROM	0x3C5DEAF9	11/25/12
0x11E	441ZL	441Z	1	Library-4 HP41Z ROM, pg 1	0x8243464F	11/29/13
0x11F	441ZU		4	Library-4 HP41Z ROM, pg 2	0x326EE777	11/29/13
0x120	LIBRARY4	4LIB	N/A	Library-4	0xBF465233	05/16/17
0x121	RAMPGX4	4RAM	17	Library-4 RAMpage X ROM	0xA181AD52	09/12/16
0x122	TBOX4L4	4TBX	13	Library-4 Toolbox ROM	0x9F0989EC	11/29/13
0x123	ALPHA44	4ALP	6	Library-4 Alpha ROM	0x09E5FFC1	01/02/17
0x124	HP67_FUN	H67G	23	HP-67 Games ROM	0x830772EB	12/14/16
0x125	OUTERPLN	OPLN	21	Outer Planets ROM	0xF5D8BB73	05/17/16
0x126	RAWGM1	RGME	18	RAW Games ROM, pg 1	0x37C08DB5	06/02/16
0x127	RAWGM2			RAW Games ROM, pg 2	0x312D0081	06/02/16
0x128	SYSDEMO1	DEMO	14	HP41 System Demo, pg 1	0xC2EFC4CA	12/12/12
0x129	SYSDEMO2			HP41 System Demo, pg 2	0xBF8D46D4	12/12/12
0x12A	SYSDEMO3			HP41 System Demo, pg 3	0x43ADC256	12/12/12
0x12B	SYSDEMO4			HP41 System Demo, pg 4	0xDDEF81D9	12/12/12
0x12C	XMASSTG	MASS	16	Extended Mass Storage ROM	0x35C45268	12/12/12
0x12D	41ZDIAG	4DIG	8	Library-4 41Z Diagnostic ROM	0x75620288	12/14/12
0x12E	PPCSTATS	PPC9	9	PPC Statistics ROM	0x58799CB2	11/10/16
0x12F	CHEMENG	CENG	12	Chemical Engineering Solutions	0x215AC3F6	01/20/13

0x130	SNEAP1	NEA1	21	SNEAP ROM, pg 1	0xBF50A7B3	10/07/12
0x131	SNEAP2		31	SNEAP ROM, pg 2	0x34536449	10/07/12
0x132	SNEAP3	NEA3	11	SNEAP ROM, pg 3	0xC66ADE2A	10/07/12
0x133	SNEAP4		10	SNEAP ROM, pg 4	0x12D67EA5	10/07/12
0x134	SNEAP5	NEA5	13	SNEAP ROM, pg 5	0x49C8C957	10/07/12
0x135	SNEAP6		14	SNEAP ROM, pg 6	0x63C507B3	10/07/12
0x136	KERMI2K	KRMK	26	Kermit 2000 ROM	0xA6D201B6	10/31/16
0x137	FLUIDDYN	FDYN	17	Fluid Dynamics Solutions	0xC5A565D3	03/03/13
0x138	SWPGAME1	GSWP	9	Swap Games ROM, pg 1	0x6C6161CB	06/03/16
0x139	SWPGAME2			Swap Games ROM, pg 2	0xDAAF84D7	06/03/16
0x13A	HVAC	HVAC	16	HVAC Solutions	0xC911BCF8	02/13/13
0x13B	SOLARENG	LENG	14	Solar Engineering Solutions	0x49D1CCD1	02/13/13
0x13C	SAMLUNG1	GRMK	10	Selections from Lung book, pg 1	0x9BB5E471	06/02/16
0x13D	SAMLUNG2			Selections from Lung book, pg 2	0xF6807B5D	06/02/16
0x13E	FAIRFLDL	FAIR	21	Fairfield ROM, pg 1	0xA76B6C13	01/27/13
0x13F	FAIRFLDU		31	Fairfield ROM, pg 2	0xCE99C45D	01/27/13
0x140	LAPLACE1	LPLC	10	Laplace Transform ROM, pg 1	0xE9CC4D9F	03/17/13
0x141	LAPLACE2			Laplace Transform ROM, pg 2	0x503F3F6B	03/17/13
0x142	RSU_OSL	ROSV	4	RSU OS ROM, pg 1	0xB0CD3BE2	04/12/13
0x143	RSU_OSU		6	RSU OS ROM, pg 2	0x61899B2C	04/12/13
0x144	HEPX4H_1	HEP2	7	HEPAX 4H ROM, bnk 1	0x16ABD09C	10/24/16
0x145	HEPX4H_2			HEPAX 4H ROM, bnk 2	0x761BDC70	02/20/16
0x146	HEPX4H_3			HEPAX 4H ROM, bnk 3	0x983EFF48	02/20/16
0x147	HEPX4H_4			HEPAX 4H ROM, bnk 4	0x216F49EC	05/08/14
0x148	BASIC1	BASI	8	BASIC ROM, pg 1	0x443C6AF4	06/04/16
0x149	BASIC2		11	BASIC ROM, pg 2	0x0CB7ABCC	06/04/16
0x14A	CMT-300L	CMT3	9	CMT-300 Multimeter ROM, pg 1	0x62DCE73E	05/08/13
0x14B	CMT-300U			CMT-300 Multimeter ROM, pg2	0x6205818C	05/08/13
0x14C	XPLORE1	GAMX	12	Explore Games ROM, pg 1	0x128417B9	06/10/16
0x14D	XPLORE2			Explore Games ROM, pg 2	0x52B2C530	06/10/16
0x14E	CMT-200	CMT2	4	CMT-200 Data Acquisition ROM	0xEC2C6C4F	05/08/13
0x14F	MLEPR_1H	EPRH	4	MLEPR -1H ROM	0x22EB5C08	05/27/13
0x150	BUDER2L	BUD2	15	Buderus-2 Module, pg 1	0x5F36C487	05/01/13
0x151	BUDER2U		16	Buderus-2 Module, pg 2	0xD1579CAA	05/01/13
0x152	BUDER3	BUD3	9	Buderus-3 Module	0x884D590E	05/01/13
0x153	MWKLN3	MWK3	10	MWK-3 Module	0x8BE45A73	05/25/13
0x154	MWKLN4L	MWK4	21	MWK-4 Module, pg 1	0x8D2CDC5A	05/01/13
0x155	MWKLN4U		31	MWK-4 Module, pg 2	0xF9D8CCE2	05/01/13
0x156	OVTROP2L	OTRP	5	Oventrop Ventil Module, pg 1	0x2FDA2E51	05/01/13
0x157	OVTROP2U		6	Oventrop Ventil Module, pg 2	0xA47B13E6	05/01/13
0x158	NEXT_2C	NEXT	6	NEXT ROM	0x9787FBD4	05/01/13
0x159	MOUNT1C	MCMP	15	Mountain Computer 1C	0x8D1D4459	05/01/13

0x15A	CMT_1	CMT1	31	CMT-100 Eprom Test ROM, pg 1	0x40C2CF42	05/25/13
0x15B	CMT_2			CMT-100 Eprom Test ROM, pg 2	0xA1C2F451	05/25/13
0x15C	CMT_3			CMT-100 Eprom Test ROM, pg 3	0x42B276C5	05/25/13
0x15D	CMT_4			CMT-100 Eprom Test ROM, pg 4	0x67C06D58	05/25/13
0x15E	ICEBOX1H	IBOX	4	ICEbox -1H ROM	0x8A1BD27C	05/25/13
0x15F	MMEPROM	EPRM	16	MMEPROM ROM	0xA9D63976	05/27/13
0x160	C141BL	141B	22	C141B FSC ROM, pg 1	0xDE1B6C26	06/19/16
0x161	C141BU		31	C141B FSC ROM, pg 2	0xB2D15B96	06/19/16
0x162	ADVTGMTH	ADVG	12	Advantage Math ROM	0xE3932EA8	06/28/16
0x163	NONLINR	NONL	16	Non-linear Systems Module	0xE0F07F5E	08/02/16
0x164	4WINS	4WIN	16	4WINS Game ROM	0x17CB9809	09/05/16
0x165	AP550-2	TGT2	10	AP550 A2 Targeting ROM	0x28A0B775	03/26/17
0x166	ALL_FF	YFSD	17	41CL SD Card Functions	0x53D36BD2	
0x167	YFNF-1E	YFNF	16	41CL Memory Functions -1E	0xD417BF8D	08/05/16
0x168	OSXB1	OSX3	5	Lib-4 OS/X Bank-Switched ROM, bnk 1	0xE382DBE5	03/23/17
0x169	OSXB2			Lib-4 OS/X Bank-Switched ROM, bnk 2	0x242C99E4	08/14/16
0x16A	OSXB3			Lib-4 OS/X Bank-Switched ROM, bnk 3	0xAA6E8246	03/24/17
0x16B	OSXB4			Lib-4 OS/X Bank-Switched ROM, bnk 4	0xEF35296D	02/07/15
0x16C	WWRAMBOX	WRAM	31	W&W Rambox64 ROM	0xDF80DEF1	07/03/13
0x16D	OBCSYS	OBCZ	31	OBCSYS ROM	0x125CDF67	08/22/13
0x16E	AECPROG	PROG	18	Program Generator ROM	0x1C33B87A	08/24/13
0x16F	AECSLVR	GSLV	18	Geometric Solver ROM	0x8BDB0A81	08/24/13
0x170	BBS1	BBSC	11	BBS ROM, pg 1	0x506EB229	08/21/13
0x171	BBS2		12	BBS ROM, pg 2	0xCB4ED3ED	08/21/13
0x172	BBS3		13	BBS ROM, pg 3	0x7D295934	08/21/13
0x173	BBS4		14	BBS ROM, pg 4	0xA441583F	08/21/13
0x174	DST12_1L	DST1	8	CalTrans Survey ROM, pg 1	0x2668BCF2	01/23/14
0x175	DST12_1U		9	CalTrans Survey ROM, pg 2	0xFF209E30	01/23/14
0x176	DST12_2L		10	CalTrans Survey ROM, pg 3	0xAA809FAD	01/23/14
0x177	ALL_00				Blank, loaded as CalTrans Survey ROM, pg 4	0xD666D0E4
0x178	MATPOL11	4MTI	22	Lib-4 Matrix/Polynomial ROM, pg 1, bnk 1	0xD0A6FF32	01/01/15
0x179	MATPOL12			Lib-4 Matrix/Polynomial ROM, pg 1, bnk 2	0xB0549E94	01/01/15
0x17A	ETSII4A	ETS4	8	ETSII4A ROM, pg 1	0xE1BDC893	12/17/16
0x17B	ETSII4B		14	ETSII4B ROM, pg 2	0x3BEA3897	01/10/17
0x17C	MATPOL21	4MTI	24	Lib-4 Matrix/Polynomial ROM, pg 2, bnk 1	0xB83FE386	01/01/15
0x17D	MATPOL22			Lib-4 Matrix/Polynomial ROM, pg 2, bnk 2	0x6B94A7E0	11/30/14
0x17E	ETSII5A	ETS5	10	ETSII5A ROM, pg 1	0xEB2004FB	12/10/16
0x17F	ETSII5B		20	ETSII5B ROM, pg2	0xDE23ECED	12/07/16
0x180	PWREXT1	PWRX	12	PowerCL Extreme, bnk 1	0x12F8AA29	08/18/16
0x181	PWREXT2			PowerCL Extreme, bnk 2	0x7E9DAE07	08/27/16
0x182	PWREXT3			PowerCL Extreme, bnk 3	0x20BE9505	08/16/16
0x183	PWREXT4			PowerCL Extreme, bnk 4	0xBF5F0F1F	02/19/16
0x184	VERMPACK	VERM	27	Vermpack 1A	0x3986AA52	05/23/14
0x185	TVM_1E	TVMY	22	TVM Module 1E	0x95BBC5C6	05/28/14
0x186	NUTIP-1A	ITCP	4	NutIP TCP/IP ROM 1A	0x993752D4	05/23/14

0x187	CARDR-1G	RCRD	30	HP-41 Card Reader ROM	0xC1604FD1	06/08/16
0x188	FORCAST1	FCST	10	Forecast ROM	0x8FCB260B	06/28/14
0x189	FORCAST2	FCS2	10	Forecast 2 ROM	0xE359A8D9	06/28/14
0x18A	CO-OPL	COOQ	31	CO-OP ROM, pg 1	0xDEE4DF51	06/28/14
0x18B	CO-OPU			CO-OP ROM, pg 2	0xD516A472	06/28/14
0x18C	SSHEET	SHTZ	8	Spreadsheet ROM	0x67DF34B4	07/16/14
0x18D	INDOOR	INDO	10	Philips Indoor Lighting ROM	0xAA5AA5CF	07/16/14
0x18E	ETSII3A	ETS3	12	ETSII3A ROM, pg 1	0x74CD8122	01/24/17
0x18F	ETSII3B			ETSII3B ROM, pg 2	0x2E2A950E	01/24/17
0x190	FCALAS1	FSSY	14	FOCAL Assmby/Disassembly ROM, pg 1	0xF315D6A5	09/08/15
0x191	FCALAS2			FOCAL Assembly/Disassembly ROM, pg 2	0xE52E0D4C	09/08/15
0x192	FOR_FEE	FFEE	14	For Fee ROM	0x5068EACF	12/13/16
0x193	ETSII6A	ETS9	16	ETSII6A ROM	0xFB2C3FED	12/15/16
0x194	CVLENG	CIVI	16	HP Civil Engineering Solutions	0xA1D22783	10/26/16
0x195	CIVU1	CIVU	20	Civil Engineering Special Collection, pg 1	0xD82A040B	08/17/14
0x196	CIVU2			Civil Engineering Special Collection, pg 2	0x75F238A0	08/17/14
0x197	CRTVONK	VONK	16	Math Programs Collection	0x19A26E5D	07/31/14
0x198	CLXPREGS	XPMM	20	CL Expanded Memory Functions	0x29D7442D	10/09/16
0x199	CCDAPPS	UCCD	18	CCD Manual examples	0x28C2D578	03/03/15
0x19A	ANGEL-1	ANGZ	3	Angel ZEPROM, pg 1	0x0144EE52	01/17/15
0x19B	ANGEL-2		12	Angel ZEPROM, pg 2	0xB4685AC1	01/17/15
0x19C	TMAX3A	TMAX	6	Turbo-MAX 3A	0x2A476430	01/17/15
0x19D	BJ1B	BLJK	7	Blackjack -1B	0x98AB118B	01/17/15
0x19E	MAX2E	BJMX	6	Blackjack MAX -2E	0x8BB1375E	01/17/15
0x19F	VEGAS1C	VEGS	6	Vegas -1C ROM	0x94DB7444	01/17/15
0x1A0	SMAT41	SM44	3	Lib-4 Sandmath 4x4 ROM, pg 1, bnk 1	0xFFEA1469	03/13/17
0x1A1	SMAT42			Lib-4 Sandmath 4x4 ROM, pg 1, bnk 2	0x14365B9D	01/24/16
0x1A2	SMAT43			Lib-4 Sandmath 4x4 ROM, pg 1, bnk 3	0xB71A1C78	05/29/15
0x1A3	SMAT44			Lib-4 Sandmath 4x4 ROM, pg 1, bnk 4	0x413BA395	03/13/17
0x1A4	HLMAT41		2	Lib-4 Sandmath 4x4 ROM, pg 2, bnk 1	0xC8CBEB21	03/07/17
0x1A5	HLMAT42			Lib-4 Sandmath 4x4 ROM, pg 2, bnk 2	0x555C8744	01/18/16
0x1A6	HLMAT43			Lib-4 Sandmath 4x4 ROM, pg 2, bnk 3	0x725C468C	01/02/17
0x1A7	HLMAT44			Lib-4 Sandmath 4x4 ROM, pg 2, bnk 4	0xA2EAA593	01/17/16
0x1A8	PPCAPPS1	PPCU	17	PPC User Applications, pg 1	0x2CC66A39	02/11/15
0x1A9	PPCAPPS2		18	PPC User Applications, pg 2	0x772F977E	02/11/15
0x1AA	WWDAPPS	WWDB	17	Wickes, Wlodek, Dearing books	0xEB6A4C4F	03/01/15
0x1AB	JARRETXF	JARR	17	K. Jarrett XF book	0xDB96270E	03/29/15
0x1AC	JARRETSP		18	K. Jarrett SP book	0x5D2B141D	03/03/15
0x1AD	RAWFL_1	GRAW	18	Gene's RAW files, pg 1	0x75391A76	02/23/15
0x1AE	RAWFL_2			Gene's RAW files, pg 2	0xF1BF6CE8	02/23/15
0x1AF	MCCRNAK	MCKK	16	Alan McCornack book	0x5B47D616	03/29/15
0x1B0	GERMAN1	KRGM	17	Kruse/Gosmann books, pg 1	0x0A1DE4B8	05/12/16
0x1B1	GERMAN2		18	Kruse/ Gosmann books, pg 2	0x70E62F06	05/12/16
0x1B2	PK_ALPH	PKP1	31	Poul Kaarup's Alpha and Pointers ROM	0xFDE520DC	04/12/15

0x1B3	PK_MATH	PKP2	14	Poul Kaarup's Math and Physics ROM, pg 1	0xC5A58BDE	04/12/15
0x1B4	PK_PHYS		15	Poul Kaarup's Math and Physics ROM, pg 2	0x64F9C986	04/12/15
0x1B5	PK_FLAG	PKP3	3	Poul Kaarup's Flags and Stack ROM	0x4E280C57	04/12/15
0x1B6	PK_PROG	PKP4	5	Poul Kaarup's Program Utilities ROM	0x744ED659	04/12/15
0x1B7	PK_TIME	PKP5	18	Poul Kaarup's Timer and Utilities ROM	0xB74C2C71	04/12/15
0x1B8	WARPB1	WARP	21	Warp-core ROM, bnk 1	0xB236C2B7	05/21/17
0x1B9	WARPB2			Warp-core ROM, bnk 2	0x8ED85B52	05/22/17
0x1BA	SWAP21	2SWP	10	Misc routines from 412 Swap Disks, pg 1	0x05843F5B	06/03/16
0x1BB	SWAP22			Misc routines from 412 Swap Disks, pg 2	0xC35B0763	06/03/16
0x1BC	HP-16C_1	16CS	16	HP-16C Emulator ROM -1A, bnk 1	0xC4C66EF6	09/06/15
0x1BD	HP-16C_2			HP-16C Emulator ROM -1A, bnk 2	0x2E986FD5	09/06/15
0x1BE	HP-16C_3			HP-16C Emulator ROM -1A, bnk 3	0xE576C6D8	09/06/15
0x1BF	HP-16C_4			HP-16C Emulator ROM -1A, bnk 4	0xED3DCB2E	09/06/15
0x1C0	HC_1	HCPL	10	Hyper-Complex Math ROM, pg 1	0xA849C145	06/01/15
0x1C1	HC_2		20	Hyper-Complex Math ROM, pg 2	0xB4C25E1C	06/01/15
0x1C2	HC_3		21	Hyper-Complex Math ROM, pg 3	0x0572EB82	06/01/15
0x1C3	ALL_00			loaded as Hyper-Complex Math ROM, pg 4	0xD666D0E4	06/01/15
0x1C4	PSYCHRO	STEQ	12	Steam Properties ROM	0x5C74CFF9	01/08/17
0x1C5	POKER	PPOK	10	Poker ROM	0xE0A23C1B	09/15/15
0x1C6	GRAVTIM1	GRVI	16	Gravity & Time ROM, pg 1	0x286F1976	10/22/15
0x1C7	GRAVTIM2			Gravity & Time ROM, pg 2	0xAF5B06D2	10/22/15
0x1C8	TS4031	TDSM	4	TDS Surveying ROM, pg 1	0xBF22F456	06/01/15
0x1C9	TS4032		10	TDS Surveying ROM, pg 2	0x9E8588CF	06/01/15
0x1CA	TS4033		31	TDS Surveying ROM, pg 3	0x6A1C50CF	06/01/15
0x1CB	TS4034		30	TDS Surveying ROM, pg 4	0x5F4775CF	06/01/15
0x1CC	TI2023	TDSI	5	TDS Instrument ROM, pg 1	0xBD37F6EA	06/01/15
0x1CD	TI2024		12	TDS Instrument ROM, pg 2	0x98D9A27A	06/01/15
0x1CE	TP2243	TDSP	8	TDS Plotter ROM, pg 1	0x9801DAC7	06/01/15
0x1CF	TP2244		9	TDS Plotter ROM, pg 2	0xF944C90F	06/01/15
0x1D0	TIDES	TIDW	10	Tides ROM	0xB2C7D535	10/04/15
0x1D1	PORTSL	PRTW	11	US Ports ROM, pg 1	0x737C5C10	10/04/15
0x1D2	PORTSU			US Ports ROM, pg 2	0x26852531	10/04/15
0x1D3	HPCALEND	CLND	12	HP Calendar Solutions	0x933814CA	10/04/15
0x1D4	RUBIKS	RUBK	8	Rubik's Cube ROM	0x264D420D	10/19/15
0x1D5	TINKER1	TAFB	21	Tinker AFB ROM, pg 1	0xD06E207D	10/19/15
0x1D6	TINKER2		31	Tinker AFB ROM, pg 2	0xE43B37DC	10/19/15
0x1D7	JMB_CAL	JMBC	17	JMB Calendar ROM	0x3B68614D	10/25/15
0x1D8	USERCAL	UCLN	18	User Calendar ROM	0xE0DAB2CA	10/25/15
0x1D9	FILTERS1	EEFD	17	EE Filter Design ROM, pg 1	0x1E8384EC	11/15/15
0x1DA	FILTERS2		18	EE Filter Design ROM, pg 2	0xCAD49825	12/15/16
0x1DB	CRYPTO41	CRTO	10	Cryptography Module	0x28BC8649	11/20/15
0x1DC	XFRAME	XBFR	30	Direct Stiffness Method, pg 1	0x02AE22A3	11/15/15
0x1DD	XBEAMS			Direct Stiffness Method, pg 2	0xF95A4251	11/15/15
0x1DE	TRUSS	XTRS	30	Direct Stiffness Method: Trusses, pg 1	0x88058D2D	11/15/15
0x1DF	XTRUSS			Direct Stiffness Method: Trusses, pg 2	0xFB3DAAD6	11/15/15

0x1E0	SWAPMTH	3SWP	12	Swapdisk Math, pg 1	0xF8896B0C	02/10/17
0x1E1	SWAPPOL		13	Swapdisk Math, pg 2	0xF3907193	02/10/17
0x1E2	XMSTAT	XTAT	6	XM Statistics Module	0x315C68DD	12/30/15
0x1E3	TRANSNEP	EPTN	23	Trans-Neptunian Planets, 2016-2025	0xB79E2C7E	11/30/15
0x1E4	COUNTRY	CITY	18	Country/Capital ROM	0x5DF3F873	12/18/15
0x1E5	SERIES	SERI	18	Sums & Series ROM	0x7D90435F	03/26/17
0x1E6	PLANETS	PLAN	9	Planets ROM	0x17FFE68E	05/10/16
0x1E7	PLUTOIDS	PLTO	10	Plutoids ROM	0xBC96B6FA	05/10/16
0x1E8	SPANISH	WORD	31	English/Spanish Dictionary ROM, pg 1	0x14567DB7	01/04/16
0x1E9	ENGLISH			English/Spanish Dictionary ROM, pg 2	0xBBFC04E6	01/04/16
0x1EA	GJM_2B_1	GJMR	31	Greg J McClure's ROM, pg 1	0x15DE7F71	08/19/16
0x1EB	GJM_2B_2			Greg J McClure's ROM, pg 2	0xC6FFD975	08/19/16
0x1EC	ELLIPTIC	ELPT	17	Elliptic ROM	0x1DD1C496	07/26/16
0x1ED	PERIOD1	HTAB	13	(HEPAX RAM only) Periodic Table, pg 1	0xCCA0B09D	02/23/16
0x1EE	PERIOD2		14	(HEPAX RAM only) Periodic Table, pg 2	0x23A57B1A	02/23/16
0x1EF	PERIOD3		16	(HEPAX RAM only) Periodic Table, pg 3	0x395E9D7C	02/23/16
0x1F0	41ZL1	Z4DL	1	41Z Deluxe, pg 1, bnk 1	0xD99F3694	03/15/17
0x1F1	41ZL2			41Z Deluxe, pg 1, bnk 2	0x79CA2B0A	03/15/17
0x1F2	ITGDIFF	FRID	18	Fractional Integro-Differentiation ROM	0x60FB8998	03/26/17
0x1F3	RECURSE	RCSN	9	Recursion ROM	0xE3D80EBC	03/16/17
0x1F4	41ZU1	Z4DL	4	41Z Deluxe, pg 2, bnk 1	0xCE112F82	02/26/17
0x1F5	41ZU2			41Z Deluxe, pg 2, bnk 2	0x7E2A062E	03/07/17
0x1F6	GAMESB1	GSB2	16	HP-41 Games Solution Book 1/2, pg 1	0x843AD40C	06/03/16
0x1F7	GAMESB2			HP-41 Games Solution Book 1/2, pg 2	0x53A51C5C	06/03/16
0x1F8	E-6A_1	E-6A	21	E-6A W&B, pg 1	0x30B8DDA3	10/31/16
0x1F9	E-6A_2		31	E-6A W&B, pg 2	0xCAEE69CA	10/31/16
0x1FA	E-6A_3		10	E-6A W&B, pg 3	0xD09ED147	10/31/16
0x1FB	E-6A_4		9	E-6A W&B, pg 4	0xB3066F07	10/31/16
0x1FC	AP550-3L	TGT3	21	AP550 A3 Targeting ROM, pg 1	0x30BD2647	03/26/17
0x1FD	AP550-3U		31	AP550 A3 Targeting ROM, pg 2	0x6079942F	03/26/17
0x1FE	ALL_FF			empty	0x53D36BD2	
0x1FF	ALL_FF			empty	0x53D36BD2	
0x800				41C Registers 0-3FF		
0x801				Registers 400-FFF (not utilized by the OS)		
0x802						
0x803						
0x804				41CL MMU registers		
0x805		YBFR		Memory Buffer		
0x806		CFDB		Memory Buffer or CFLDB location		
0x807						
0x808						
0x809						
0x80A						
0x80B						
0x80C						

0x80D						
0x80E						
0x80F						
0x810				Page 0 of Sector Buffer		
0x811				Page 1 of Sector Buffer		
0x812				Page 2 of Sector Buffer		
0x813				Page 3 of Sector Buffer		
0x814				Page 4 of Sector Buffer		
0x815				Page 5 of Sector Buffer		
0x816				Page 6 of Sector Buffer		
0x817				Page 7 of Sector Buffer		
0x818						
0x819						
0x81A						
0x81B						
0x81C						
0x81D						
0x81E						
0x81F						
0x820						
0x821						
0x822						
0x823						
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0x825						
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NOTES:

1. Flash memory starts at page 0x000, and RAM memory starts at page 0x800.

2. A blank page in Flash memory is different from an empty page in Flash memory. A blank page contains 0x0000 in every location, and can be safely plugged into a Port. An empty (erased or unprogrammed) page contains 0xFFFF in every location, which will lock up the 41CL when plugged into a Port. A blank page has a YCRC value of 0xD666D0E4, while an empty page has a YCRC value of 0x53D36BD2.

3. Flash memory is always erased by sectors. Normally a sector consists of eight pages, and sector boundaries are highlighted in the table. Either the the top sector or the bottom sector is unique, and is erased by subsector. Refer to the “41CL Flash Updates” document for the details.

RAM Memory Details

Physical Address	Contents	Used for
0x800000 - 0x800003	Register 000	T register
0x800004 - 0x800007	Register 001	Z register
0x800008 - 0x80000B	Register 002	Y register
0x80000C - 0x80000F	Register 003	X register
0x800010 - 0x800013	Register 004	L register
0x800014 - 0x800017	Register 005	ALPHA register 1-7
0x800018 - 0x80001B	Register 006	ALPHA register 8-14
0x80001C - 0x80001F	Register 007	ALPHA register 15-21
0x800020 - 0x800023	Register 008	ALPHA register 22-28
0x800024 - 0x800027	Register 009	temp ALPHA scratch
0x800028 - 0x80002B	Register 00A	OS functions
0x80002C - 0x80002F	Register 00B	
0x800030 - 0x800033	Register 00C	
0x800034 - 0x800037	Register 00D	
0x800038 - 0x80003B	Register 00E	FLAG register
0x80003C - 0x80003F	Register 00F	OS functions
0x800040 - 0x8000FF	Registers 10 - 3F	not visible to OS
0x800100 - 0x800103	Register 040	X-Memory
0x800104 - 0x800107	Register 041	
.	.	
.	.	
0x8002F8 - 0x8002FB	Register 0BE	
0x8002FC - 0x8002FF	Register 0BF	
0x800300 - 0x800303	Register 0C0	
0x800304 - 0x800307	Register 0C1	Key Assign Buffer, Alarm Buffer, I/O Buffers, Program memory Data
.	.	
.	.	
0x8007F8 - 0x8007FB	Register 1FE	
0x8007FC - 0x8007FF	Register 1FF	
0x800800 - 0x800803	Register 200	not visible to OS
0x800804 - 0x800807	Register 201	X-Memory
0x800808 - 0x80080B	Register 202	
.	.	
.	.	
0x800BB8 - 0x800BBB	Register 2EE	
0x800BBC - 0x800BBF	Register 2EF	
0x800BC0 - 0x800C03	Registers 2F0 - 300	not visible to OS

0x800C04 - 0x800C07	Register 301		X-Memory
0x800C08 - 0x800C0B	Register 302		
.	.		
0x800FB8 - 0x800FBB	Register 3EE		
0x800FBC - 0x800FBF	Register 3EF		
0x800FC0 - 0x800FFF	Registers 3F0 - 3FF		not visible to OS
0x801000 - 0x801FFF	Registers 400 - 7FF		Expanded Memory 1
0x802000 - 0x802FFF	Registers 800 - BFF		Expanded Memory 2
0x803000 - 0x803FFF	Registers C00 - FFF		Expanded Memory 3
0x804000	41C Register Address		REGADD
0x804001 - 0x80400B	reserved		
0x80400C	Page 0 MMU registers (only one bank)	set 0	Page 0 MMU data
0x80400D		set 1	
0x80400E		set 2	
0x80400F		set 3	
0x804010 - 0x804017	41CL Extra/Extreme functions internal use		
0x804018	Turbo Mode storage location		Light Sleep
0x804019	41CL Extra/Extreme functions internal use		
0x80401A	CPU ST[14:8] storage location		Light Sleep
0x80401B	reserved		
0x80401C	Page 1 MMU register (only one bank)	set 0	Page 1 MMU data
0x80401D		set 1	
0x80401E		set 2	
0x80401F		set 3	
0x804020 - 0x80402B	41CL Memory Functions internal use		
0x80402C	Page 2 MMU register (only one bank)	set 0	Page 2 MMU data
0x80402D		set 1	
0x80402E		set 2	
0x80402F		set 3	
0x804030	Page 4 MMU register dynamic storage		
0x804031 - 0x80403B	41CL Update Functions internal use		
0x80403C	Page 3 MMU register (only one bank)	set 0	Page 3 MMU data
0x80403D		set 1	
0x80403E		set 2	
0x80403F		set 3	

0x804040	Page 4, Bank 1 MMU registers	set 0	Page 4 MMU data
0x804041		set 1	
0x804042		set 2	
0x804043		set 3	
0x804044	Page 4, Bank 3 MMU registers	set 0	
0x804046		set 1	
0x804046		set 2	
0x804047		set 3	
0x804048	Page 4, Bank 2 MMU registers	set 0	
0x804049		set 1	
0x80404A		set 2	
0x80404B		set 3	
0x80404C	Page 4, Bank 4 MMU registers	set 0	
0x80404D		set 1	
0x80404E		set 2	
0x80404F		set 3	
0x804050 - 0x804053	Page 5, Bank 1 MMU registers	sets 0-3	Page 5 MMU data
0x804054 - 0x804057	Page 5, Bank 3 MMU registers	sets 0-3	
0x804058 - 0x80405B	Page 5, Bank 2 MMU registers	sets 0-3	
0x80405C - 0x80405F	Page 5, Bank 4 MMU registers	sets 0-3	
0x804060 - 0x804063	Page 6, Bank 1 MMU registers	sets 0-3	Page 6 MMU data
0x804064 - 0x804067	Page 6, Bank 3 MMU registers	sets 0-3	
0x804068 - 0x80406B	Page 6, Bank 2 MMU registers	sets 0-3	
0x80406C - 0x80406F	Page 6, Bank 4 MMU registers	sets 0-3	
.	.		.
0x8040E0 - 0x8040E3	Page E, Bank 1 MMU registers	sets 0-3	Page E MMU data
0x8040E4 - 0x8040E7	Page E, Bank 3 MMU registers	sets 0-3	
0x8040E8 - 0x8040EB	Page E, Bank 2 MMU registers	sets 0-3	
0x8040EC - 0x8040EF	Page E, Bank 4 MMU registers	sets 0-3	
0x8040F0 - 0x8040F3	Page F, Bank 1 MMU registers	sets 0-3	Page F MMU data
0x8040F4 - 0x8040F7	Page F, Bank 3 MMU registers	sets 0-3	
0x8040F8 - 0x8040FB	Page F, Bank 2 MMU registers	sets 0-3	
0x8040FC - 0x8040FF	Page F, Bank 4 MMU registers	sets 0-3	
0x804100 - 0x804FFF	41CL functions internal use		
0x805000 - 0x805FFF	Memory Buffer (1 4Kx16 page)		
0x806000 - 0x806FFF	CFLDB location		
0x807000 - 0x80FFFF	User RAM (9 4Kx16 pages)		
0x810000 - 0x817FFF	Sector Buffer (8 4Kx16 pages)		
0x818000 - 0x87FFFF	User RAM (104 4Kx16 pages)		

NOTES:

1. Each seven-byte 41C register is stored in four successive memory locations:

bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
addr lsb																
00	Byte 1								Byte 0							
01	Byte 3								Byte 2							
10	Byte 5								Byte 4							
11	unused								Byte 6							

2. Each MMU entry is stored in a single memory location:

bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	EN	LCK	MULTI	A23	A22	A21	A20	A19	A18	A17	A16	A15	A14	A13	A12	

Bit 15 is the Enable (EN) bit. If this bit is zero, the MMU entry is ignored and the corresponding page and bank will be fetched from a Port.

Bit 14 is the Lock (LCK) bit, used only with the 41CL Extreme Functions. If this bit is set to one the entry cannot be changed, except with either the **UNLOCK** function or the **MMUCLR** function.

Bits 13 and 12 are the Multi-page Image (MULTI) bits, used only with the 41CL Extreme Functions. These bits are managed automatically by the **PLUG** and **PPLUG** functions, with the following meanings:

13	12	MULTI
0	0	Not part of a multi-page image
0	1	First page of a multi-page image
1	1	Middle page of a multi-page image
1	0	Last page of a multi-page image

Bits 11-0 hold the twelve address bits to be substituted for the Page address (bits 15-12) portion of the logical address, to create the physical address.

3. MMU register sets 1-3 are used only by the 41CL Extreme Functions.

